

/ Descriptions

PDFN 3×3A-8L

N

MOS

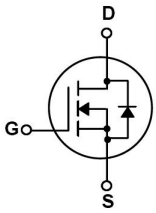
x

/ Features

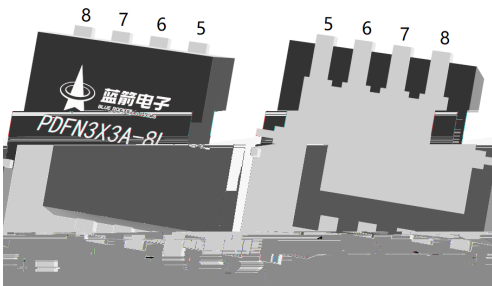
AEC-Q101

/ Applications

/ Equivalent Circuit



/ Pinning



出脚	定义
Pin1	S
Pin2	S
Pin3	S
Pin4	G
Pin5	D
Pin6	D
Pin7	D
Pin8	D

/ Marking

/ Electrical Characteristic Curve

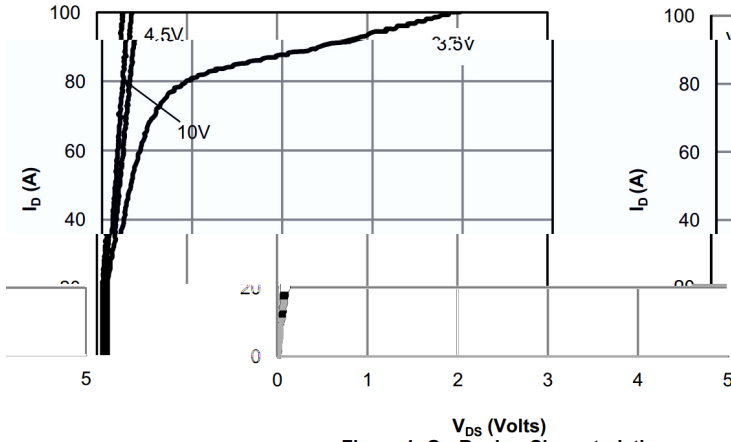


Figure 1: On-Region Characteristics

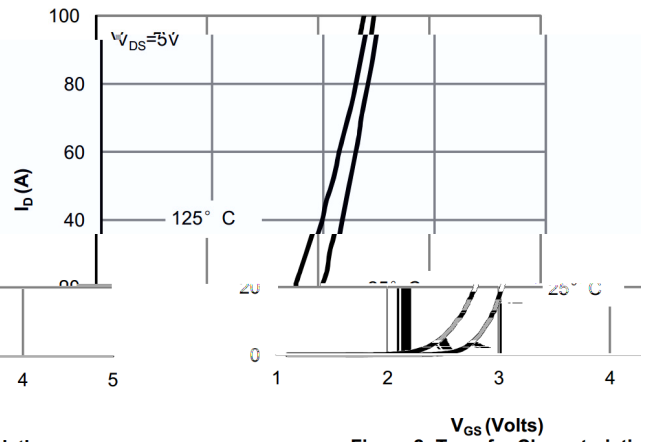


Figure 2: Transfer Characteristic

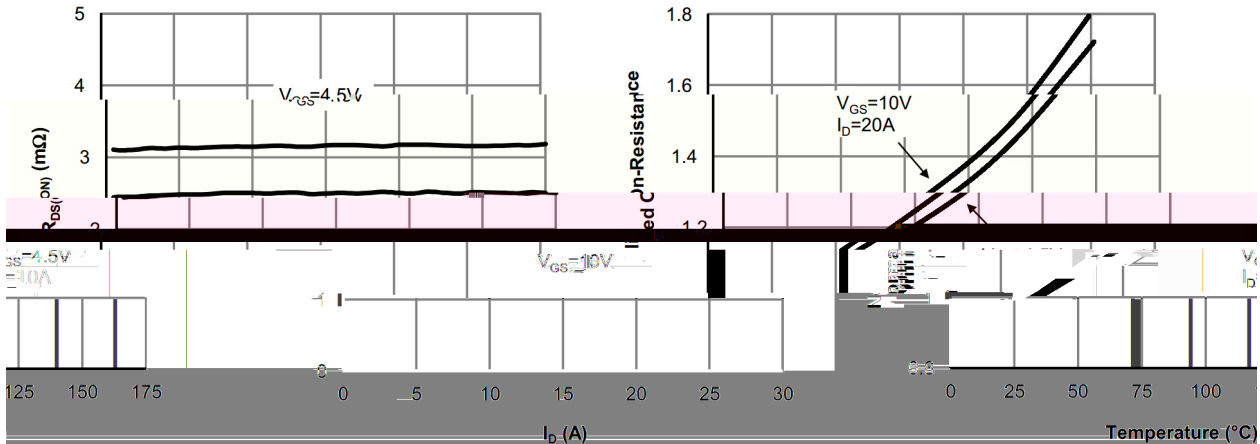


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

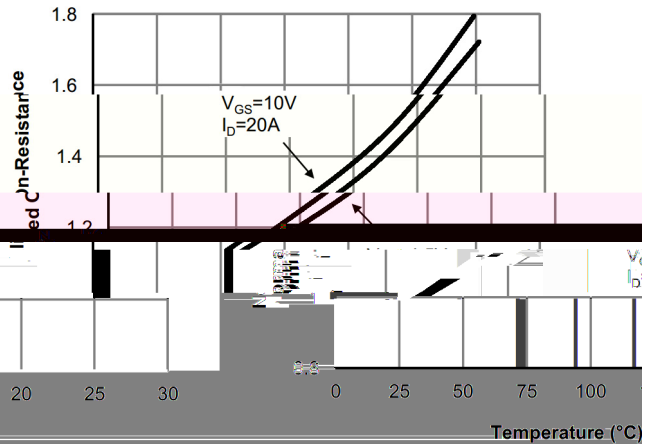


Figure 4: On-Resistance vs. Junction Temperature

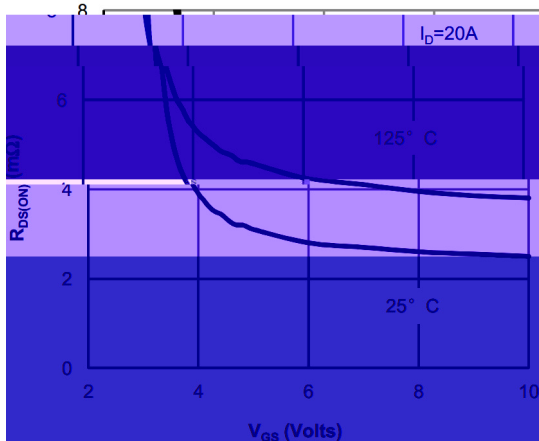


Figure 5: On-Resistance vs. Gate-Source Voltage

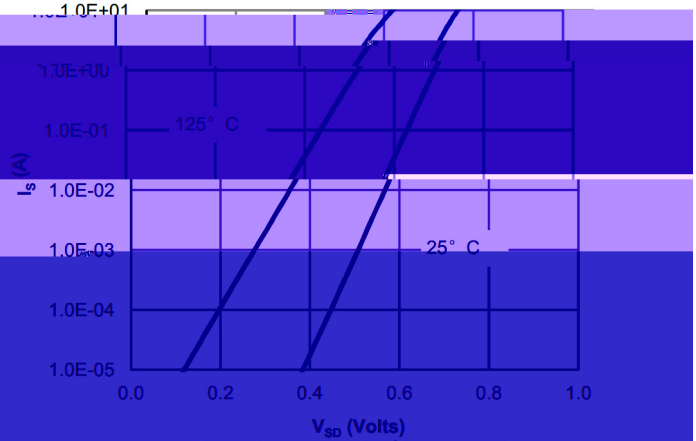


Figure 6: Body-Diode Characteristics

/ Electrical Characteristic Curve

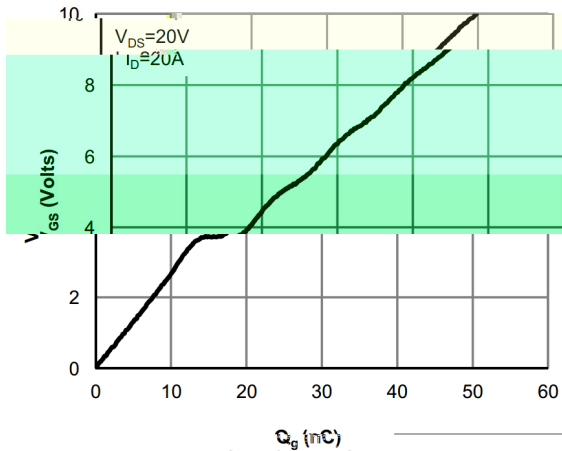


Figure 7: Gate-Charge Characteristics

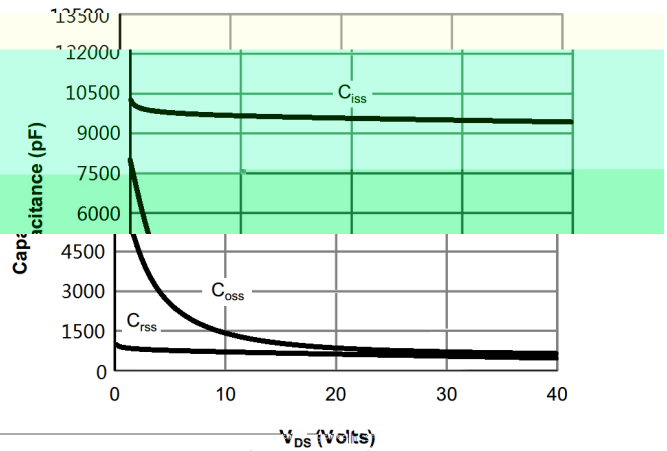


Figure 8: Capacitance Characteristics

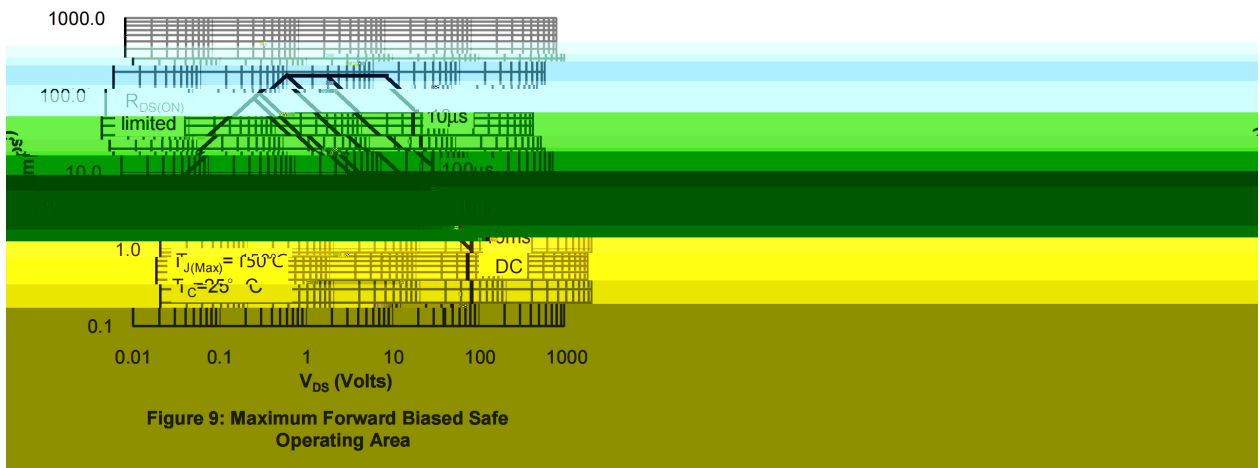
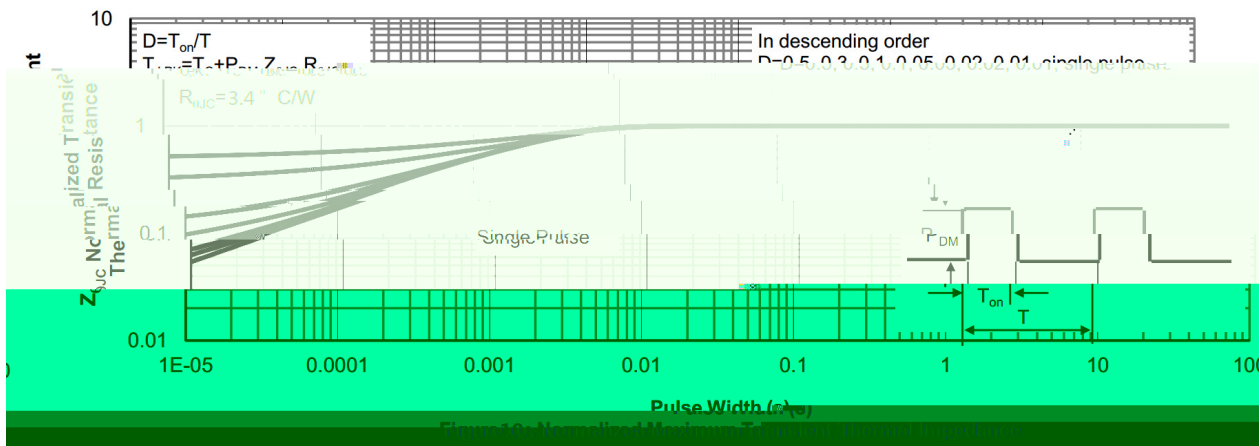


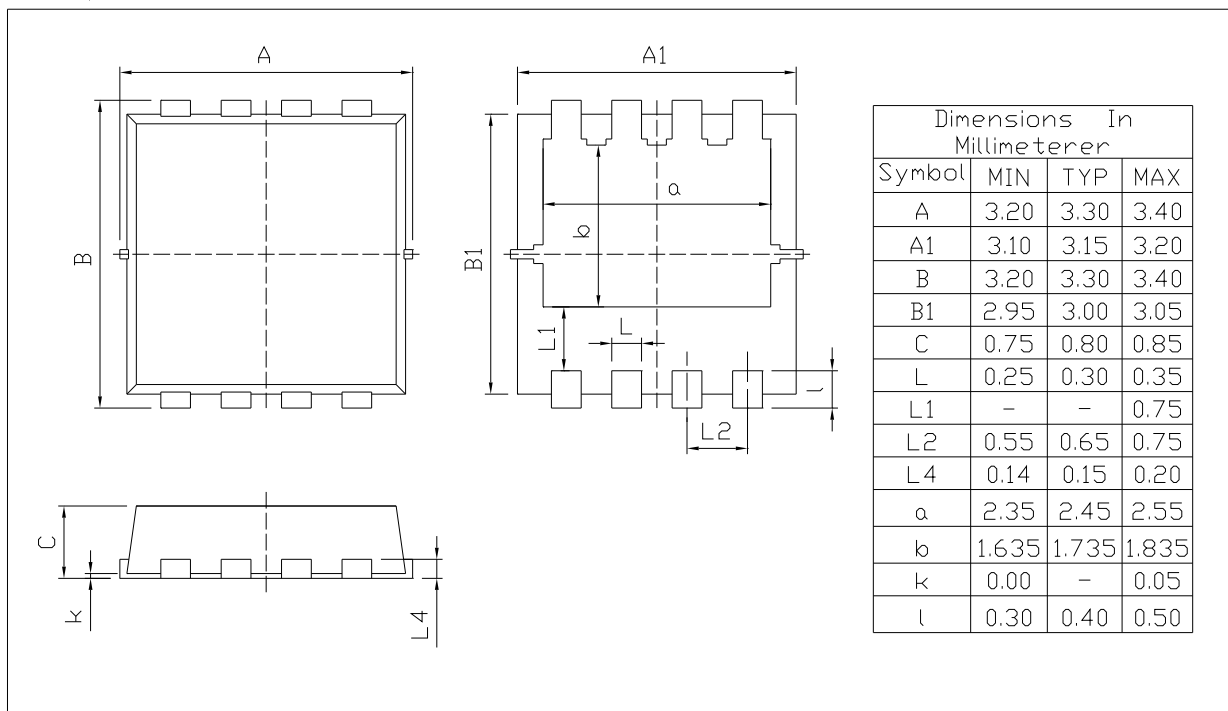
Figure 9: Maximum Forward Biased Safe Operating Area



/ Package Dimensions

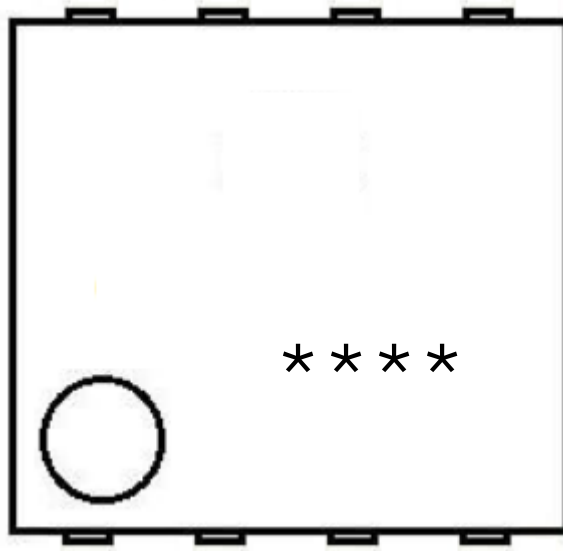
PDFN3X3A-8L

Unit:mm

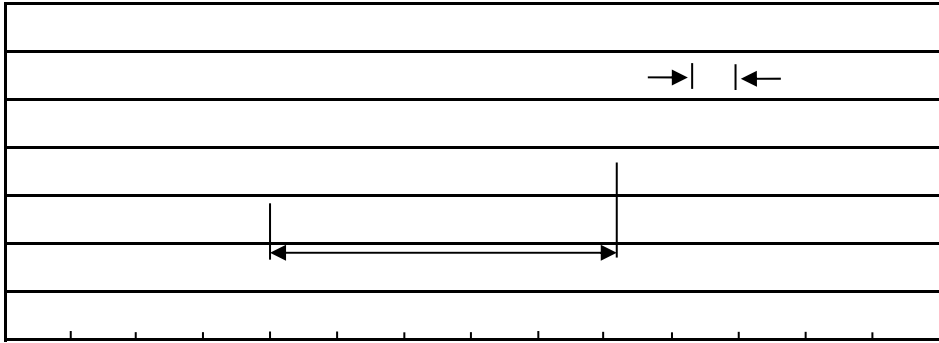


Rev.00 202011

/ Marking Instructions



() / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ± ± ±

/ Packaging SPEC.

/ Notices