

/ Descriptions

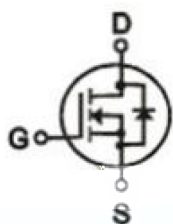
N-Channel MOSFET in a TOLL-8L Plastic Package .

/ Features

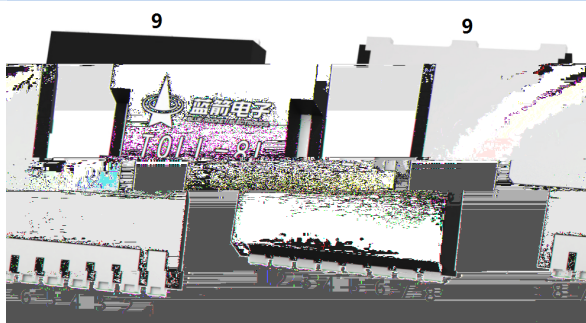
$\leq \Omega \quad \Omega$

/ Applications

/ Equivalent Circuit



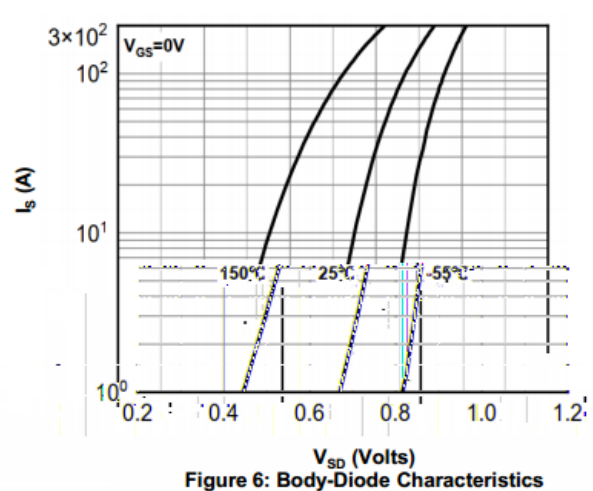
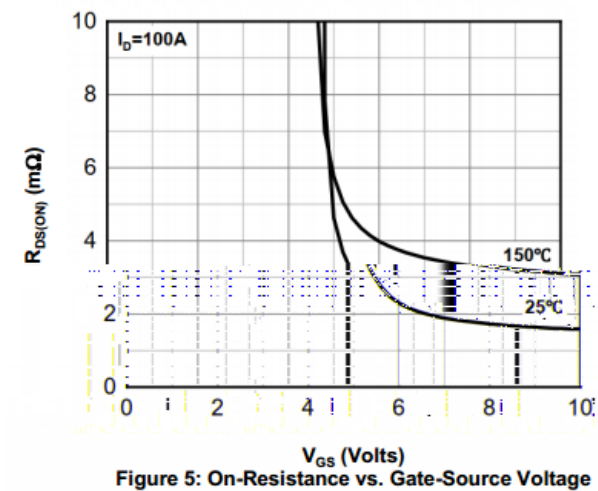
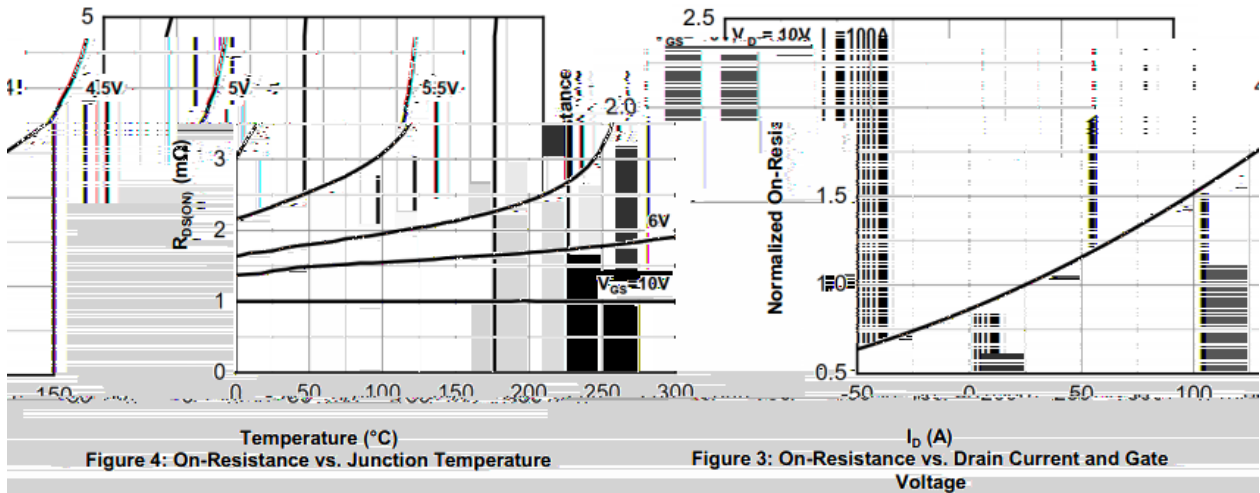
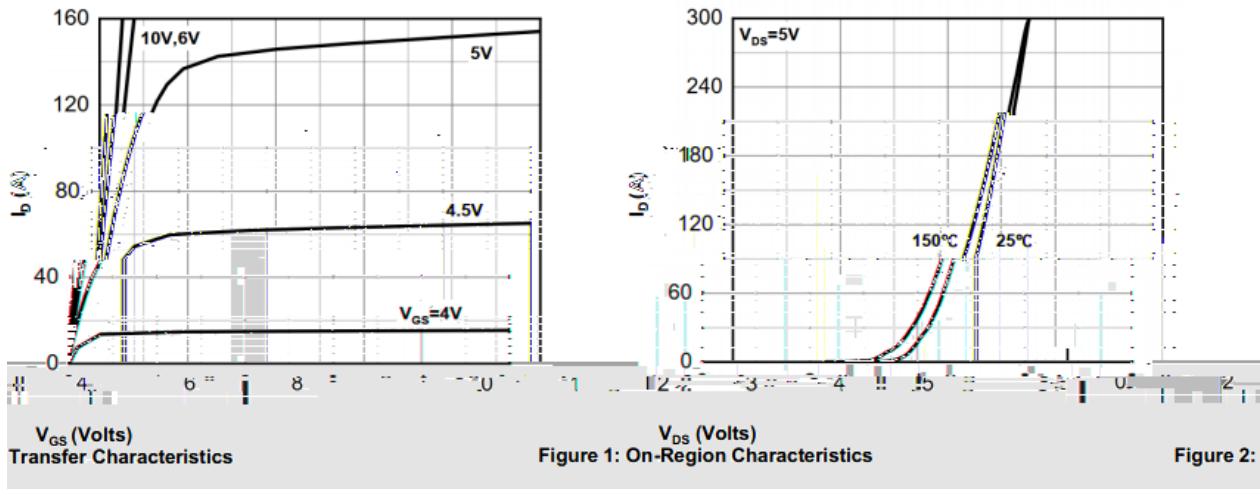
/ Pinning



/ Marking

/ Electrical Characteristics(Ta=25)

/ Electrical Characteristic Curve



/ Electrical Characteristic Curve

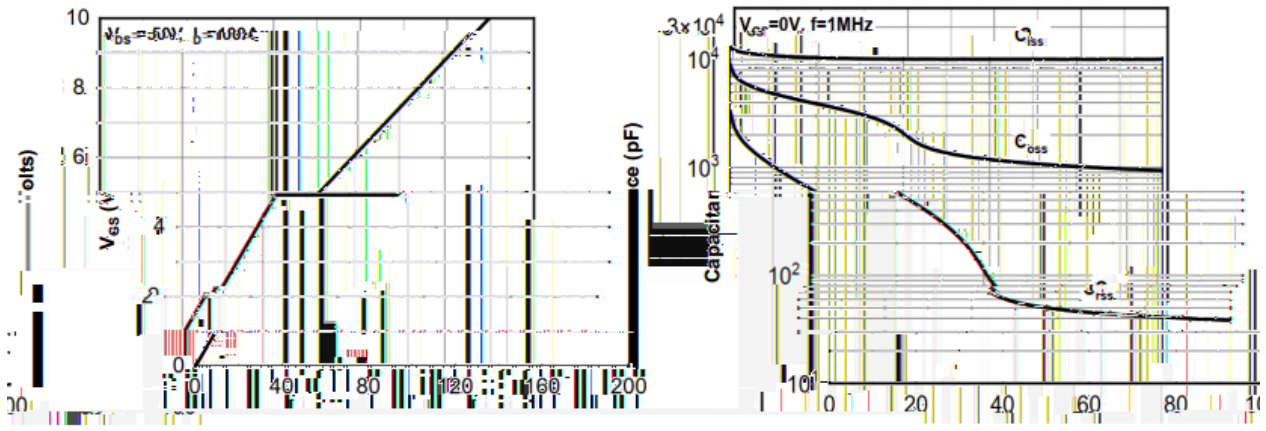


Figure 7: Gate-Charge Characteristics Figure 8: Capacitance Characteristics

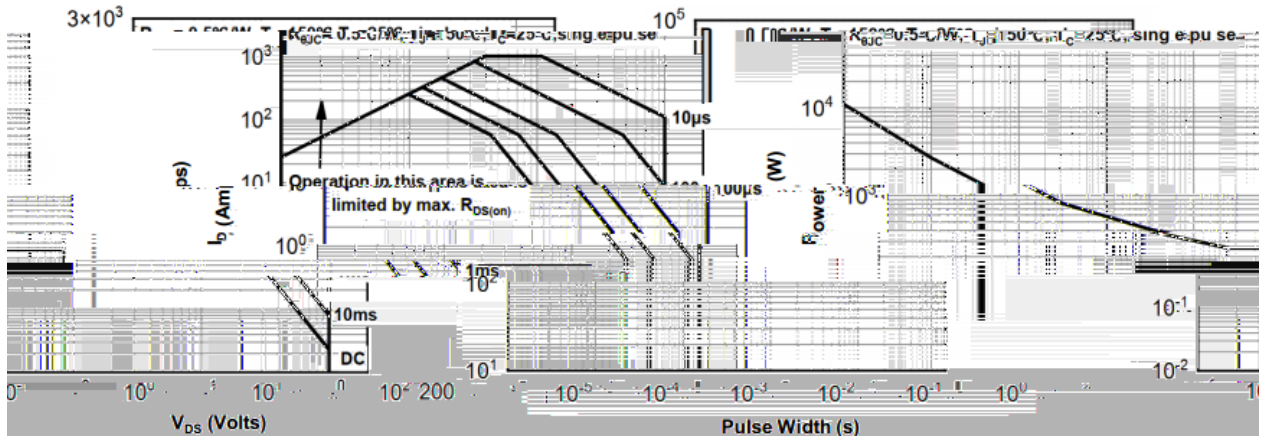


Figure 9: Maximum Forward Biased Safe Operating Area Figure 10: Single Pulse Power Rating Junction-to-Case

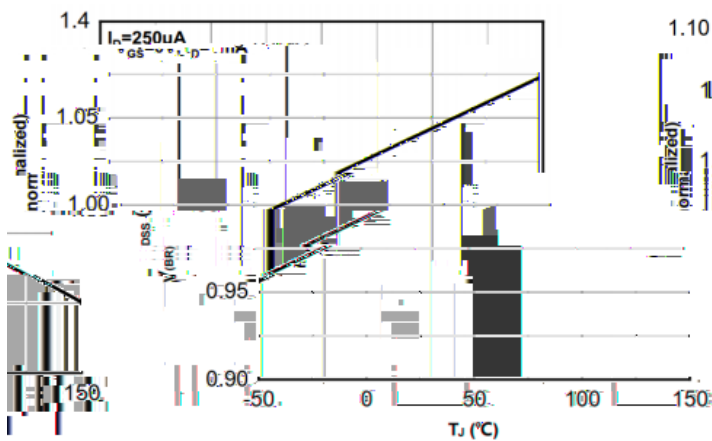


Figure 12: Normalized V(BR)DSS vs. Junction Temperature

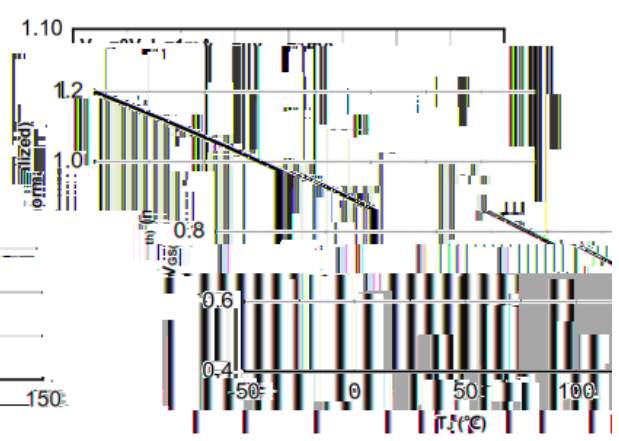
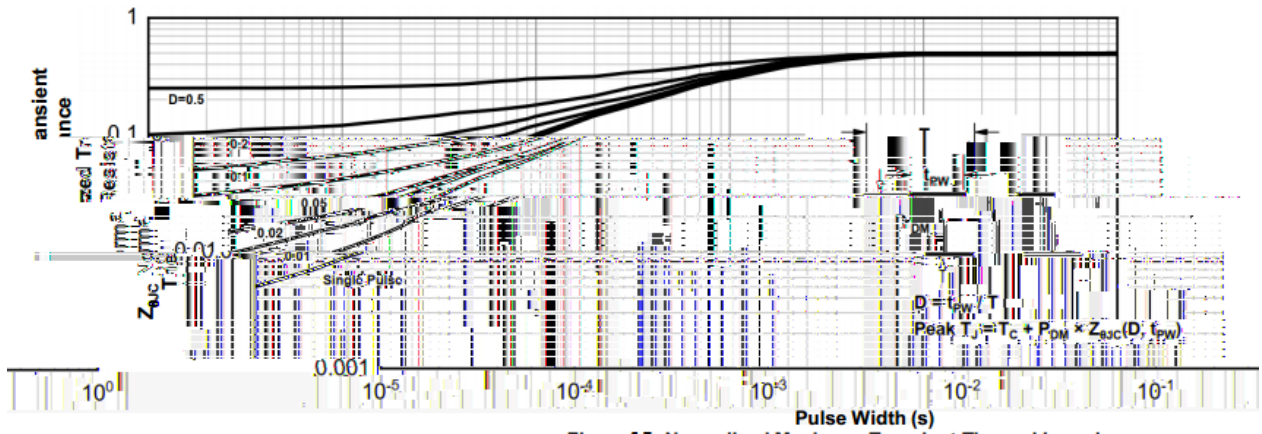
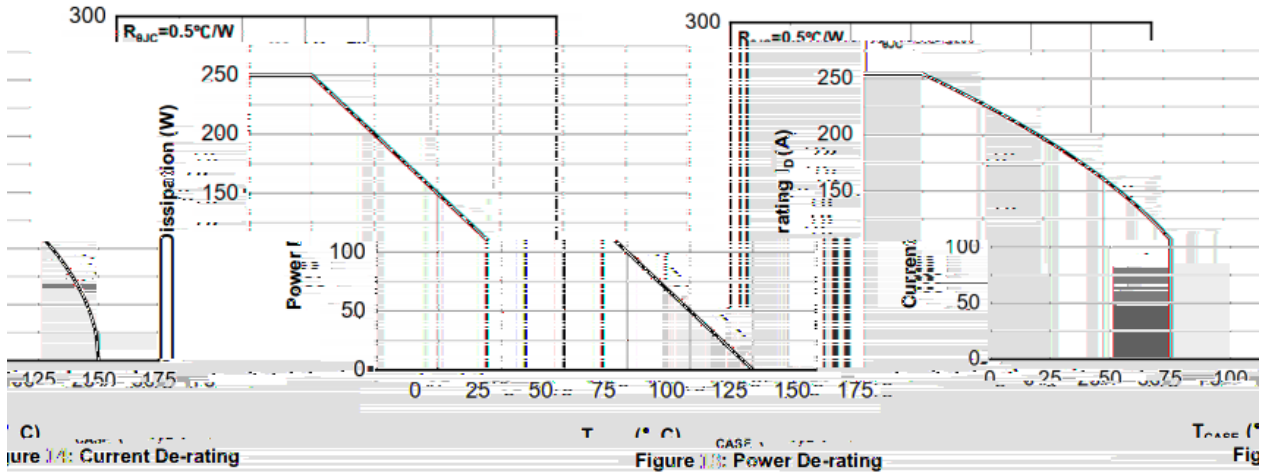
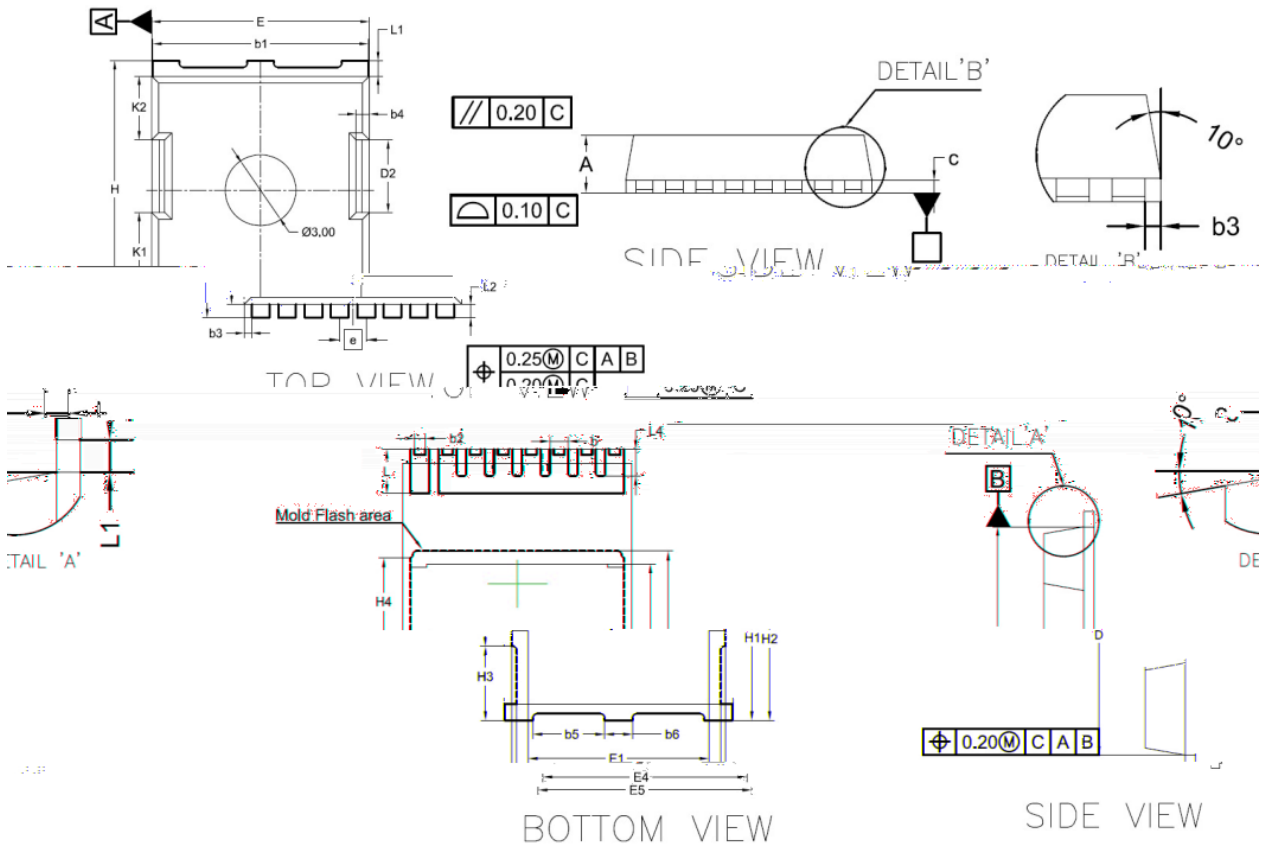


Figure 11: Normalized VGS(th) vs. Junction Temperature

/ Electrical Characteristic Curve

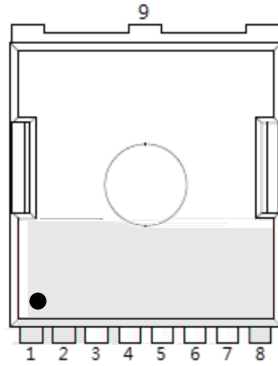


/ Package Dimensions



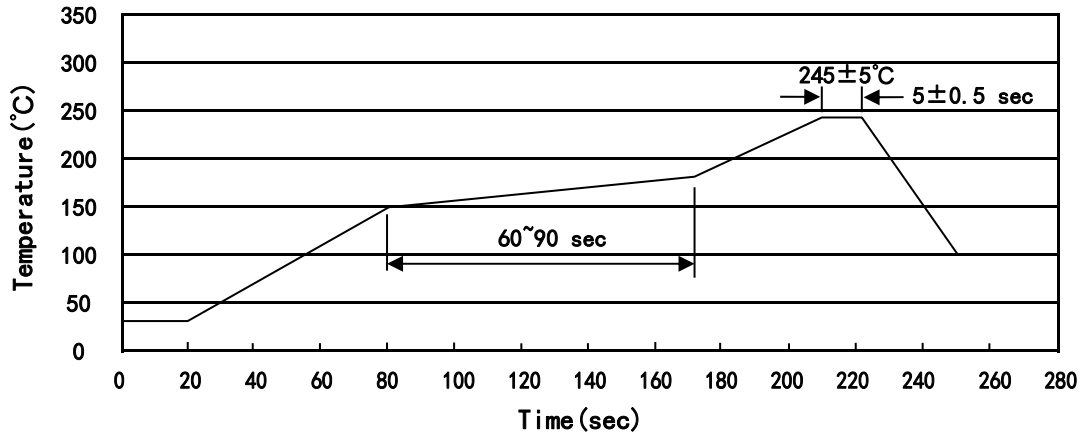
Symbol	Dimensions In Millimeters			Symbol	Dimensions In Millimeters		
	MIN.	NOM.	MAX.		MIN.	NOM.	MAX.
A	2.200	2.300	2.400	b1	9.700	9.800	9.900
c	0.492	0.500	0.508	b1	0.420	0.460	0.500
D	10.280	10.380	10.480	b3	0.350		
E	9.800	9.900	10.000	b4	0.600		
e	1.20 BSC			b5	3.100		
H	11.580	11.680	11.780	b6	1.200		
H1	6.650	6.750	6.850	L	1.700	1.900	2.100
H2	7.300			L1	0.700		
H3	3.200			L2	0.600		
H4	3.800			L4	1.050	1.150	1.250
K1	4.180			L5	0.500	0.600	0.700
K2	2.900			E1	7.800		
D2	3.300			E4	8.800		
b	0.700	0.800	0.900	E5	9.200		

/ Marking Instructions



BR

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



± ± ± ±

/ Resistance to Soldering Heat Test Conditions

± ±

/ Packaging SPEC.

	Units/Reel /	Reels/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Reel	Inner Box	Outer Box

/ Notices